

U.S. Serial No. 10/673,608

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IN THE CLAIMS

Please amend Claim 24 as follows.

1-15. (Canceled).

16. (Previously Presented) An apparatus for making a correspondence between digital information in electric signal form and information in magnetic signal form recorded in helical tracks on a magnetic recording tape, comprising:

a magnetic transducing head;

a head driving mechanism which causes said magnetic transducing head to scan on said helical tracks from a preamble portion disposed at a lower side of said magnetic recording tape preceding a digital signal information portion that includes a plurality of data blocks, each data block including a first header signal containing a first synchronizing signal and a first address signal; and

wherein a plurality of second header signals are recorded in said preamble portion, and each of said second header signals includes a second synchronizing signal, and a second address signal in continuity with said first address signal.

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17. (Previously presented) The apparatus of claim 16, wherein each of said header signals further includes a parity signal for correcting an error.

18. (Previously presented) The apparatus of claim 17, wherein each of said header signals further includes an identification signal for controlling said digital information.

19. (Previously presented) The apparatus of claim 16, wherein said control circuit further determines and protects a synchronous state based on said header signals already transduced.

20. (Previously Presented) An apparatus for recording digital information into helical tracks on a magnetic tape, comprising:

a rotary magnetic head;

a head driving mechanism which causes said rotary magnetic head to scan on said helical tracks from a preamble portion disposed at a lower side of said magnetic recording tape preceding a digital signal information portion that includes a plurality of data blocks, each data

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block including a first header signal containing a first synchronizing signal and a first address signal; and

a control circuit which controls a recording sequence to record second header signals spaced apart in said preamble portion before recording said digital information on each of said helical tracks,

wherein each of said second header signals includes a second synchronizing signal, corresponding to said first synchronizing signal, and a second address signal in continuity with said first address signal.

21. (Previously presented) The apparatus of claim 20, wherein each of said header signals further includes a parity signal for correcting an error.

22. (Previously presented) The apparatus of claim 21, wherein each of said header signals further includes an identification signal for controlling said digital information.

23. (Previously presented) The apparatus of claim 21, wherein said control circuit records said header signals for protecting a synchronous state in reproducing mode.

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24. (Currently Amended) A digital signal recording apparatus for recording while forming tracks in a helical form by rotating head on a magnetic tape; said apparatus comprising:

a generation processing unit ~~for dividing which divides~~ a digital signal into a plurality of data blocks, ~~for generating processing digital signal data in which a first synchronizing signal for indicating at least a start position of the data block and first header information including each of said data blocks having a header signal including a first synchronizing signal for indicating start position of the data block and~~ a first address signal indicating order of the data blocks, ~~are wherein each of said data blocks is~~ arranged so that a predetermined time interval is disposed between successive synchronizing signals;

a generation unit ~~for generating which generates~~ a preamble signal preceding to said ~~digital signal data data blocks~~ on the time axis, and ~~disposing a plurality of second synchronizing signals corresponding to said first synchronizing signal, and a said preamble signal having a plurality of second header signals, each of said second header signals including a information portion including a second address signal indicating order of said second synchronizing signals~~ signal corresponding to said first synchronizing

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signal and a second address signal indicating order of said  
second synchronizing signals having continuity with said first  
address signals; and

a recording unit ~~for recording which records said~~  
~~data blocks including said first synchronizing signal and said~~  
~~first address signal from within said digital signal data and~~  
~~said first address signal at a lower side of a track on the~~  
tape, and which further recording records said preamble signal  
including in continuity with said digital signal data, and  
~~recording said second synchronizing signal signals with said~~  
~~second address signal signals at a further lower side of the~~  
~~recording position on said track of said first synchronizing~~  
~~signal and first address signal~~ said data blocks.